

Figure 1

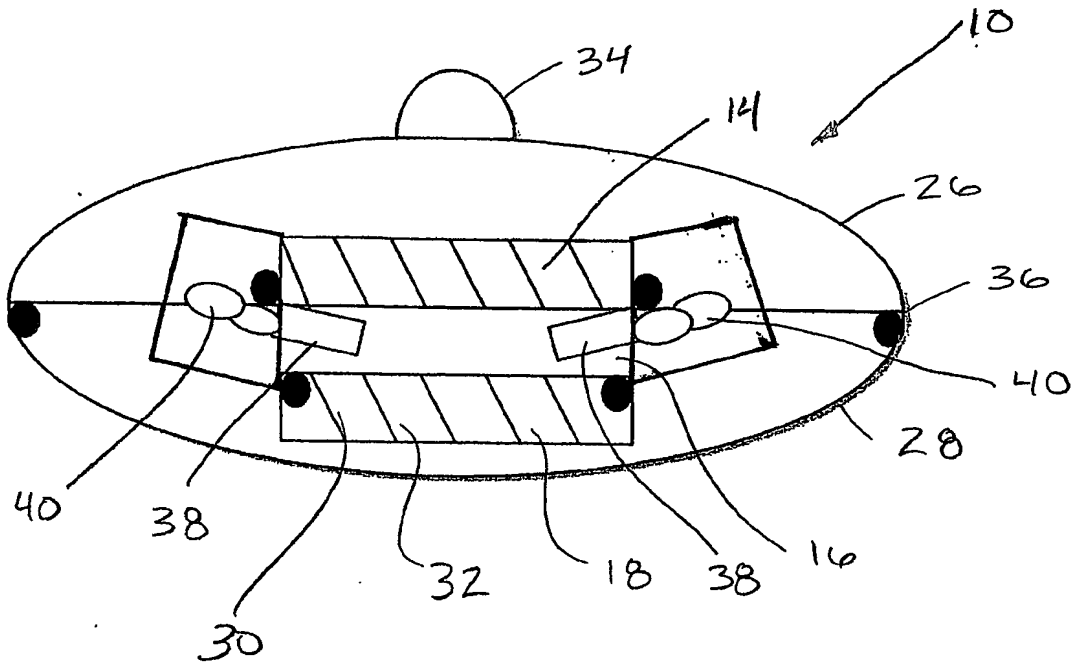
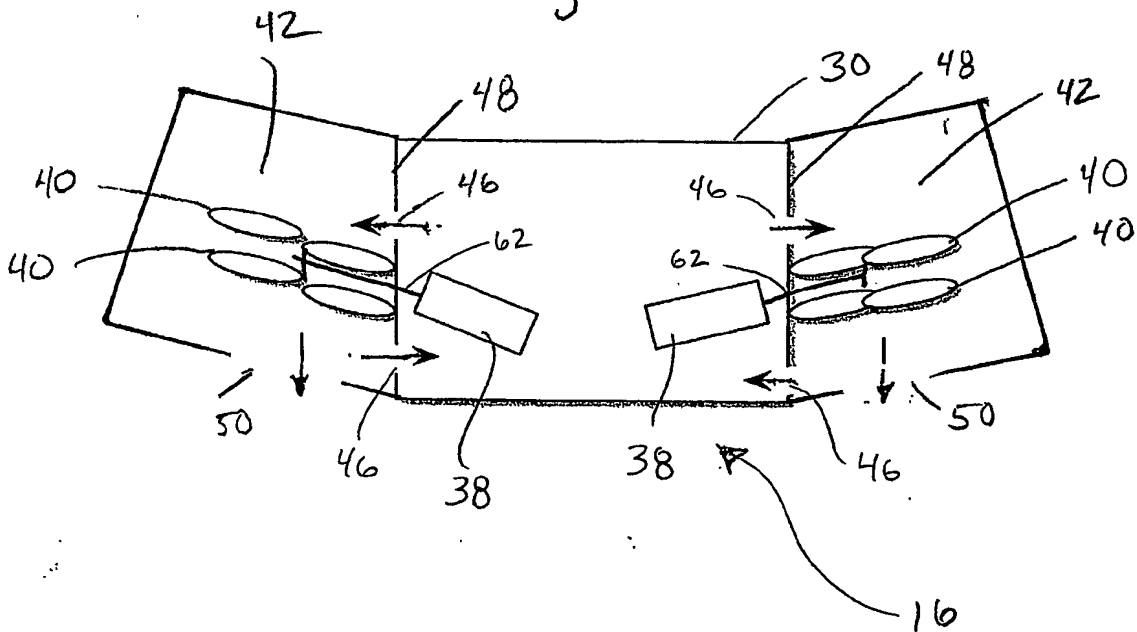
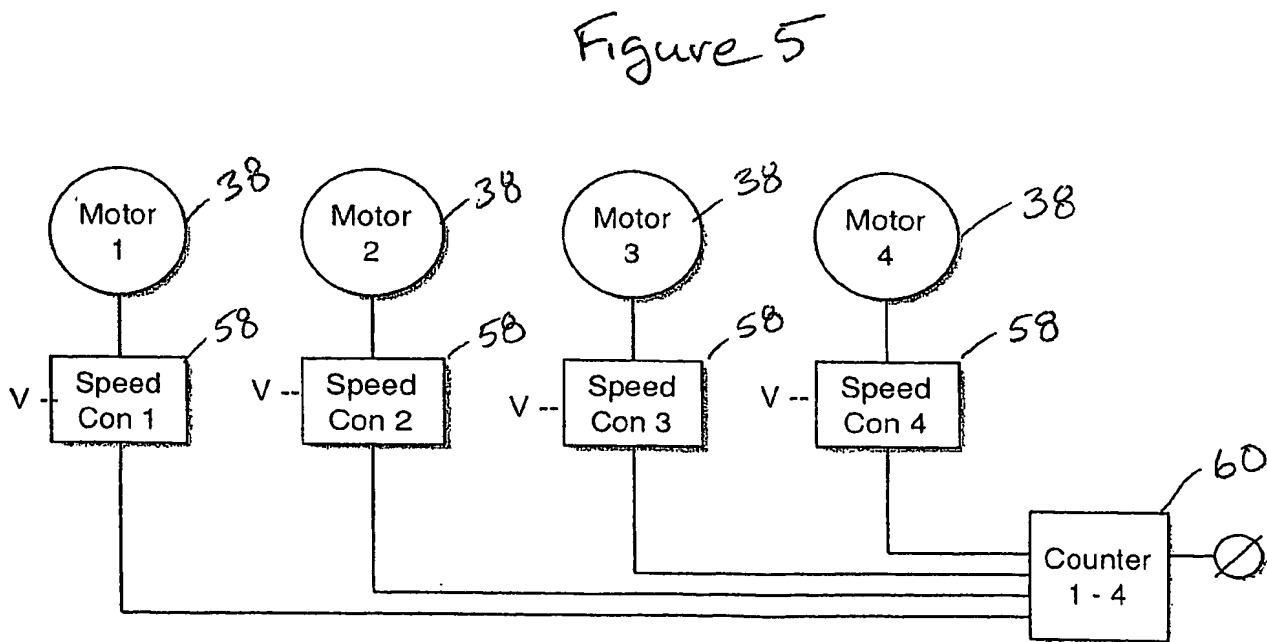
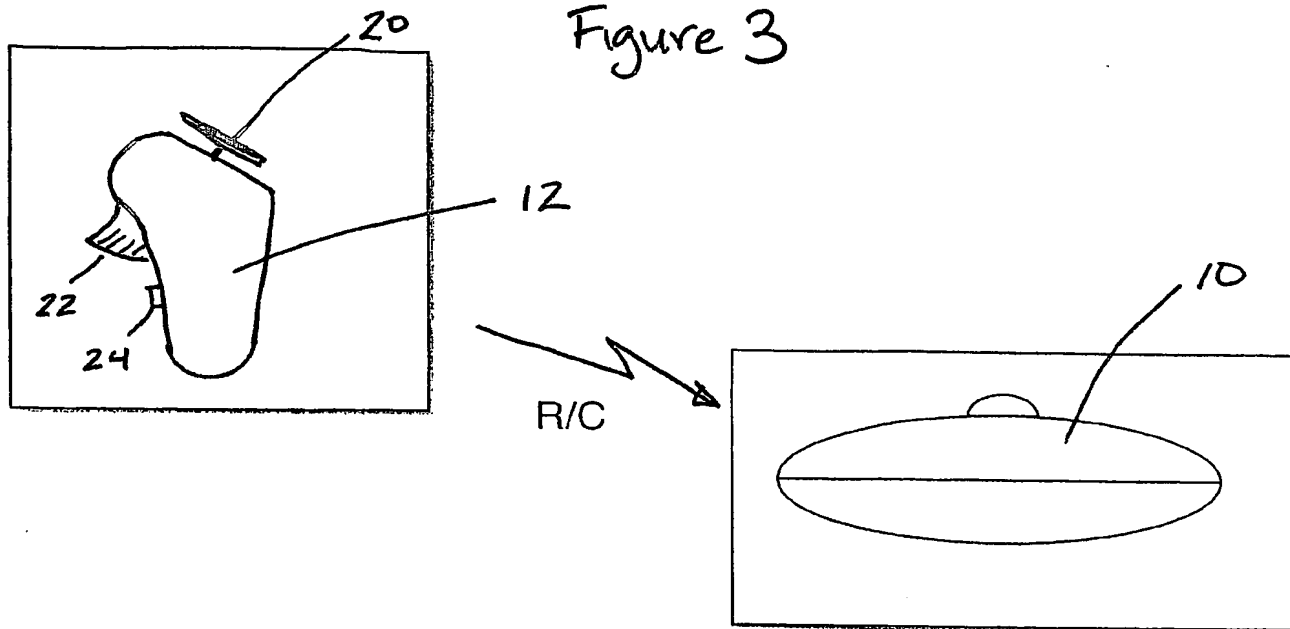


Figure 2



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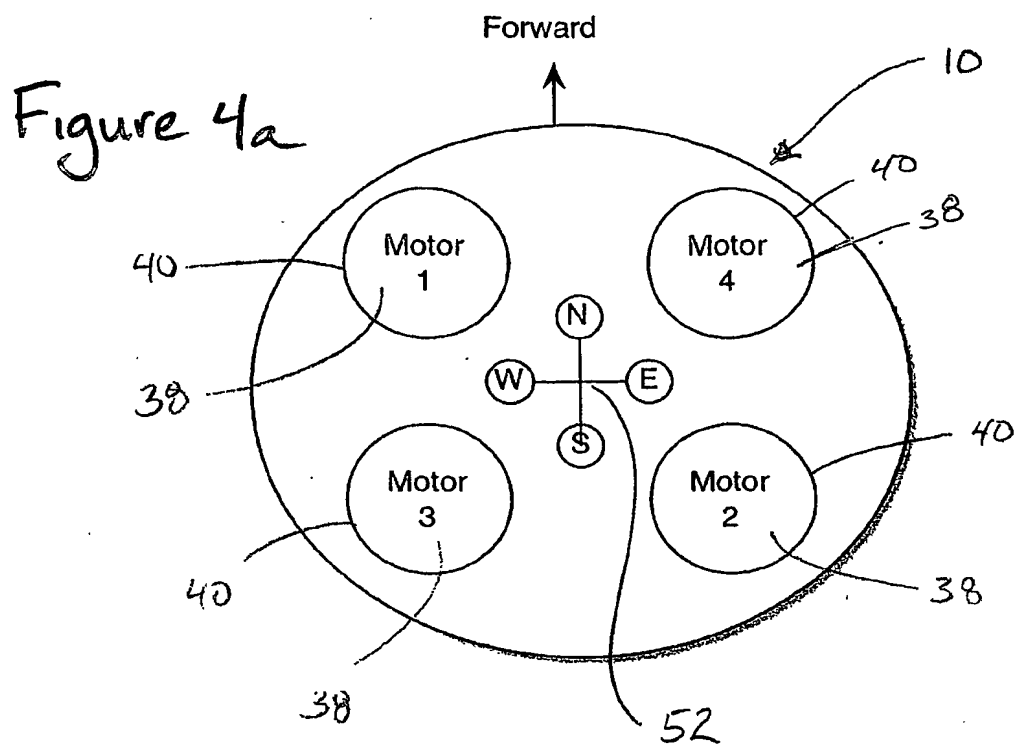
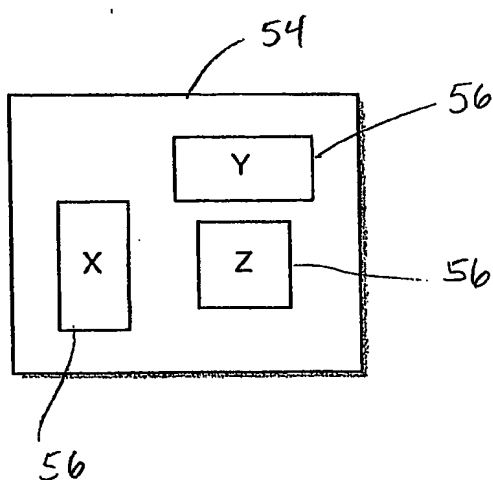


Figure 4b



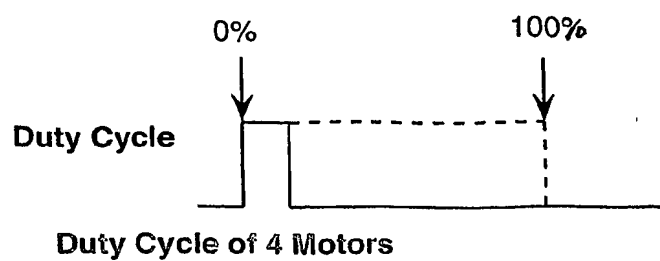


Figure 6

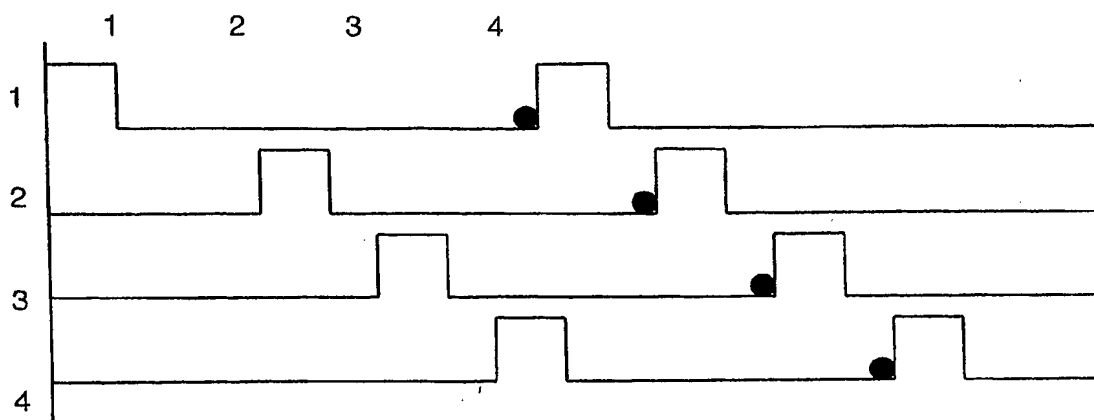


Figure 7

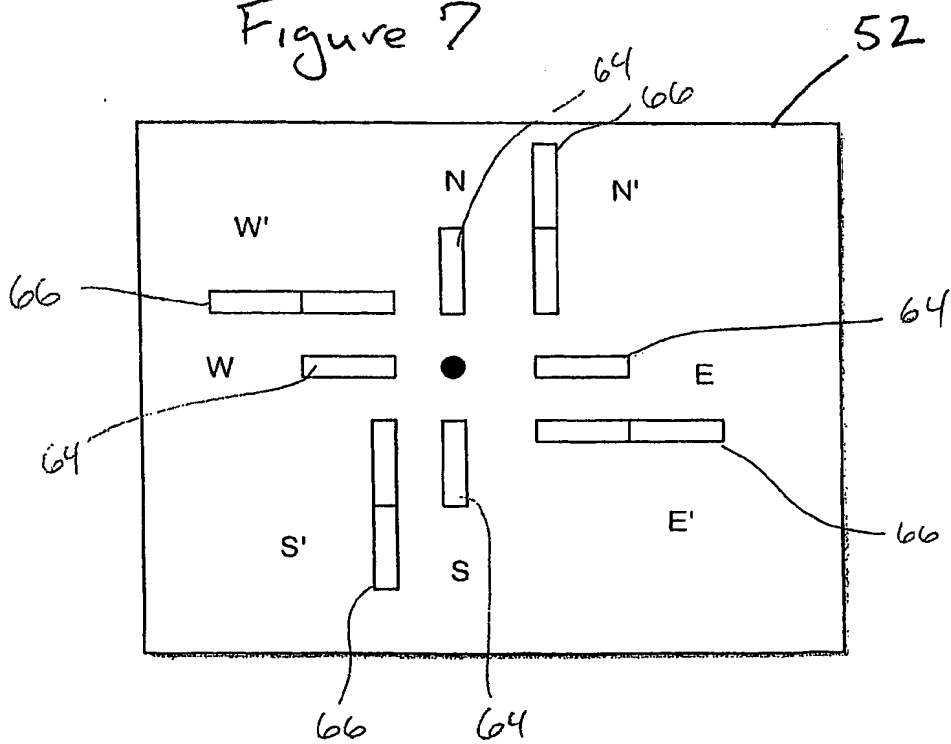


Figure 8

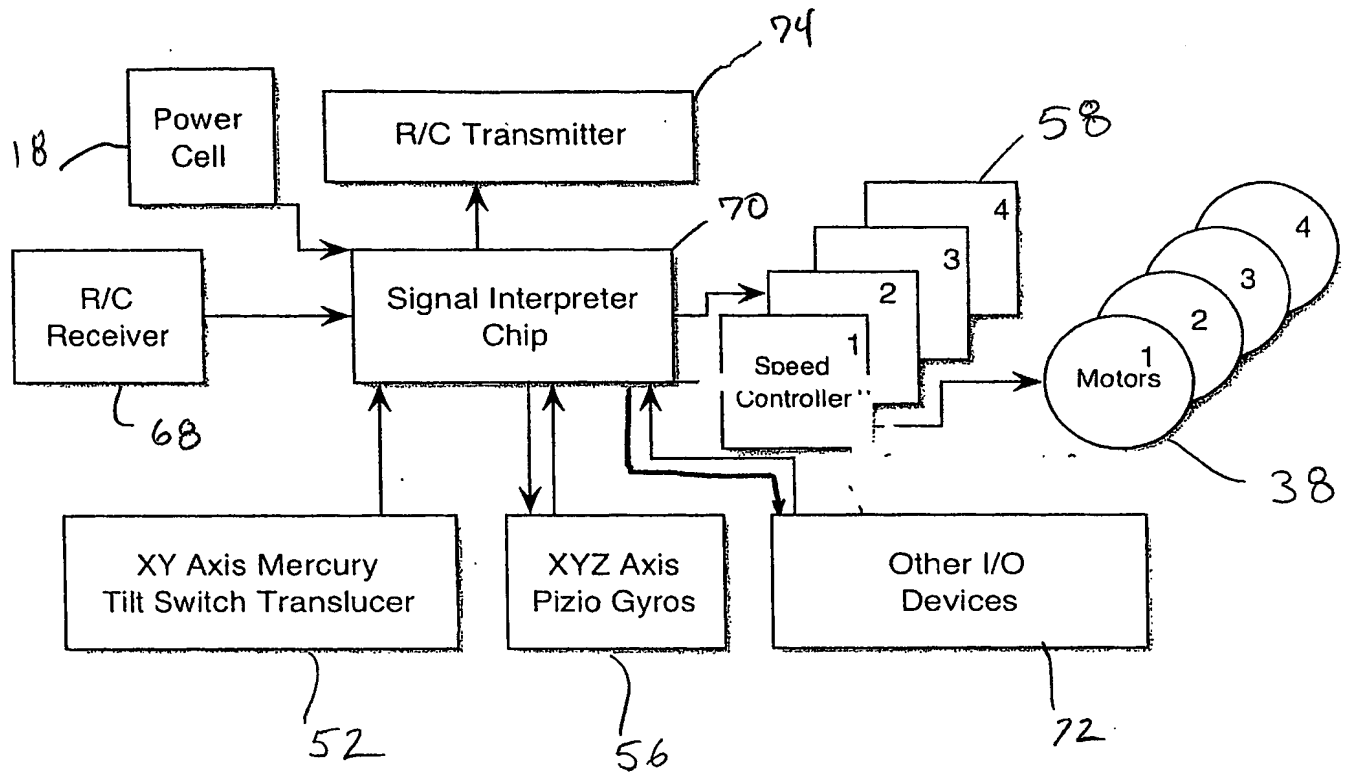


Figure 9

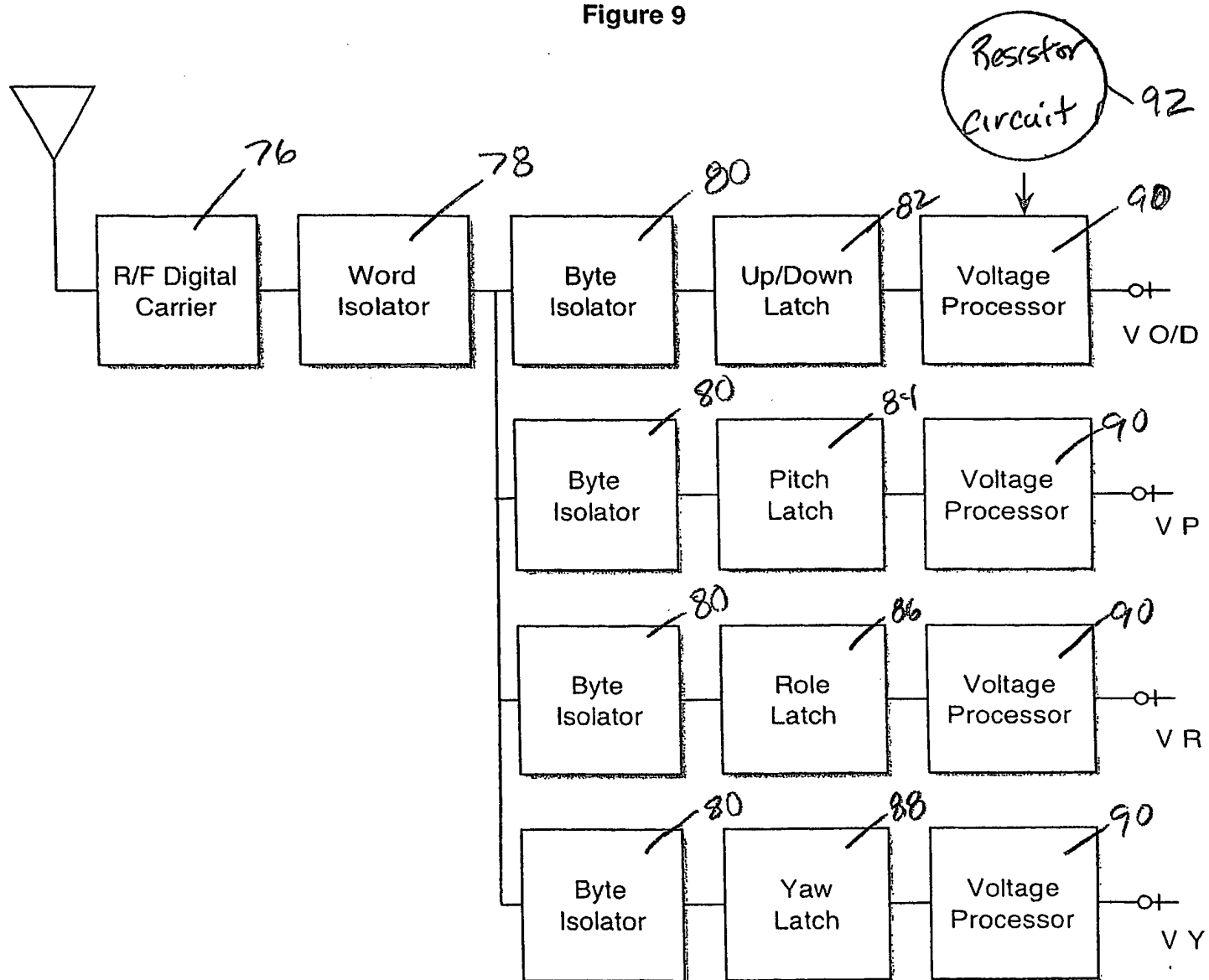


Figure 10

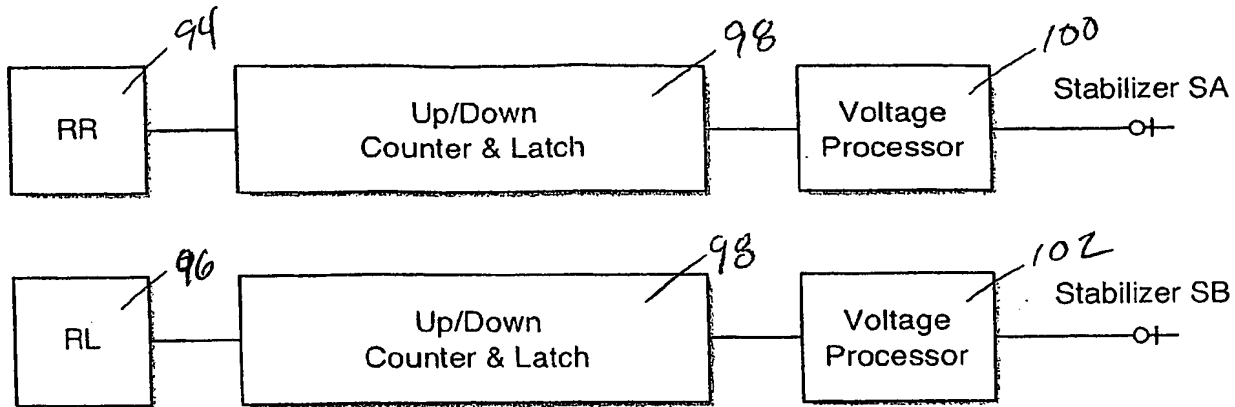


Figure 12

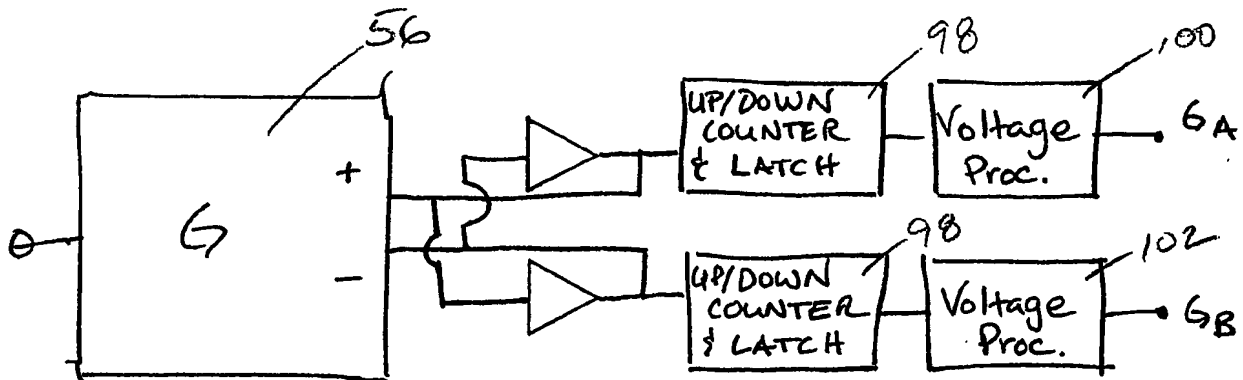


Figure 11

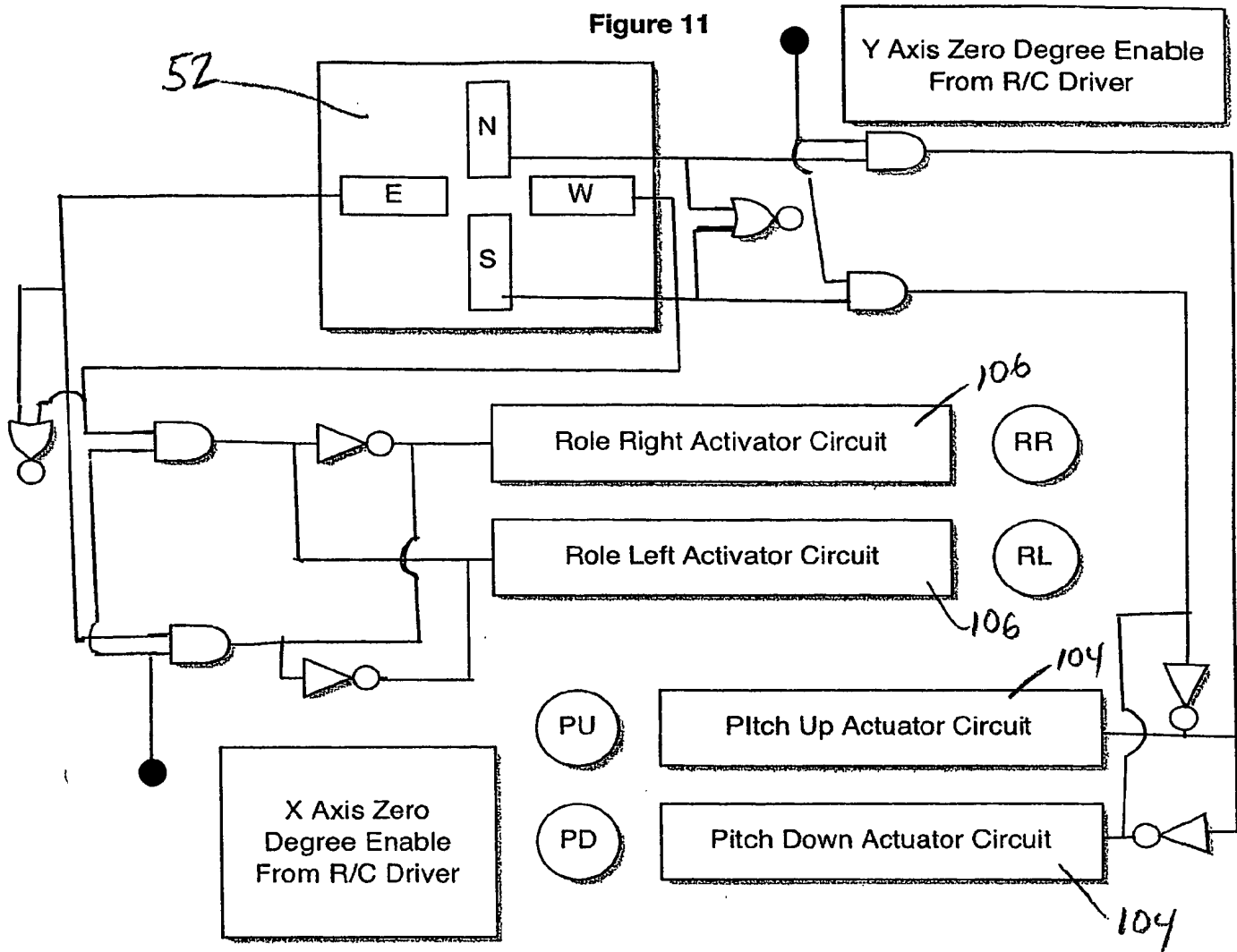
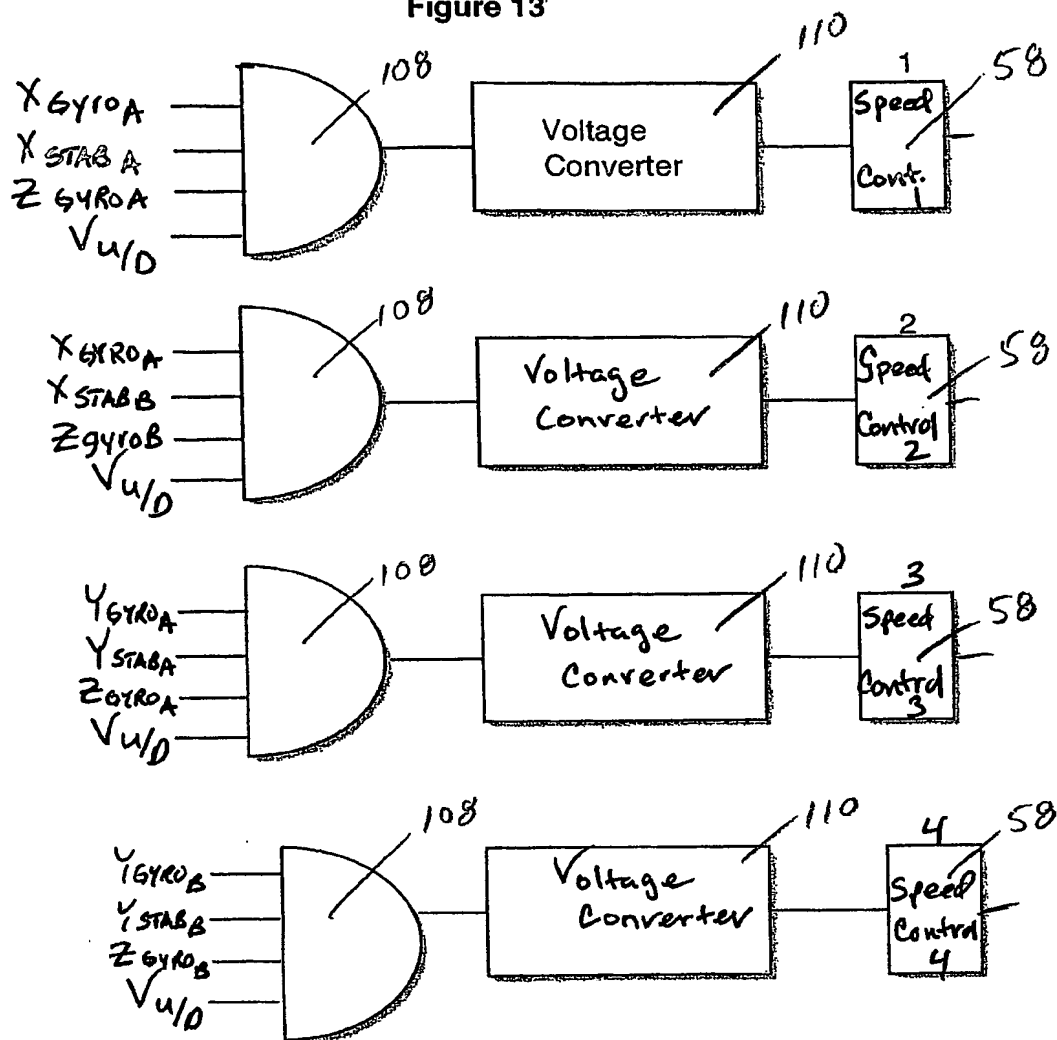


Figure 13



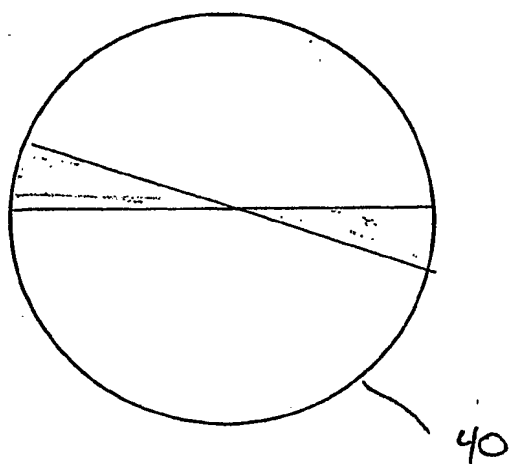


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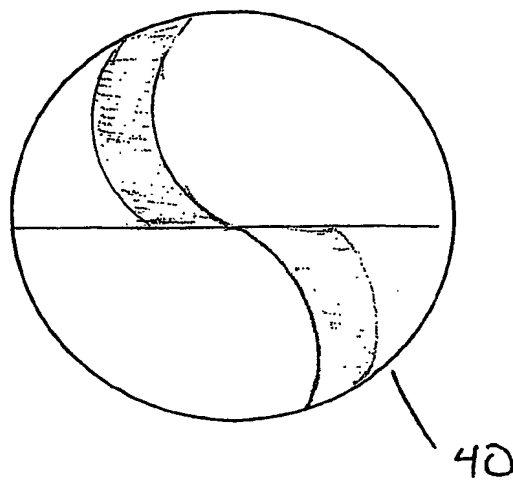


Figure 15

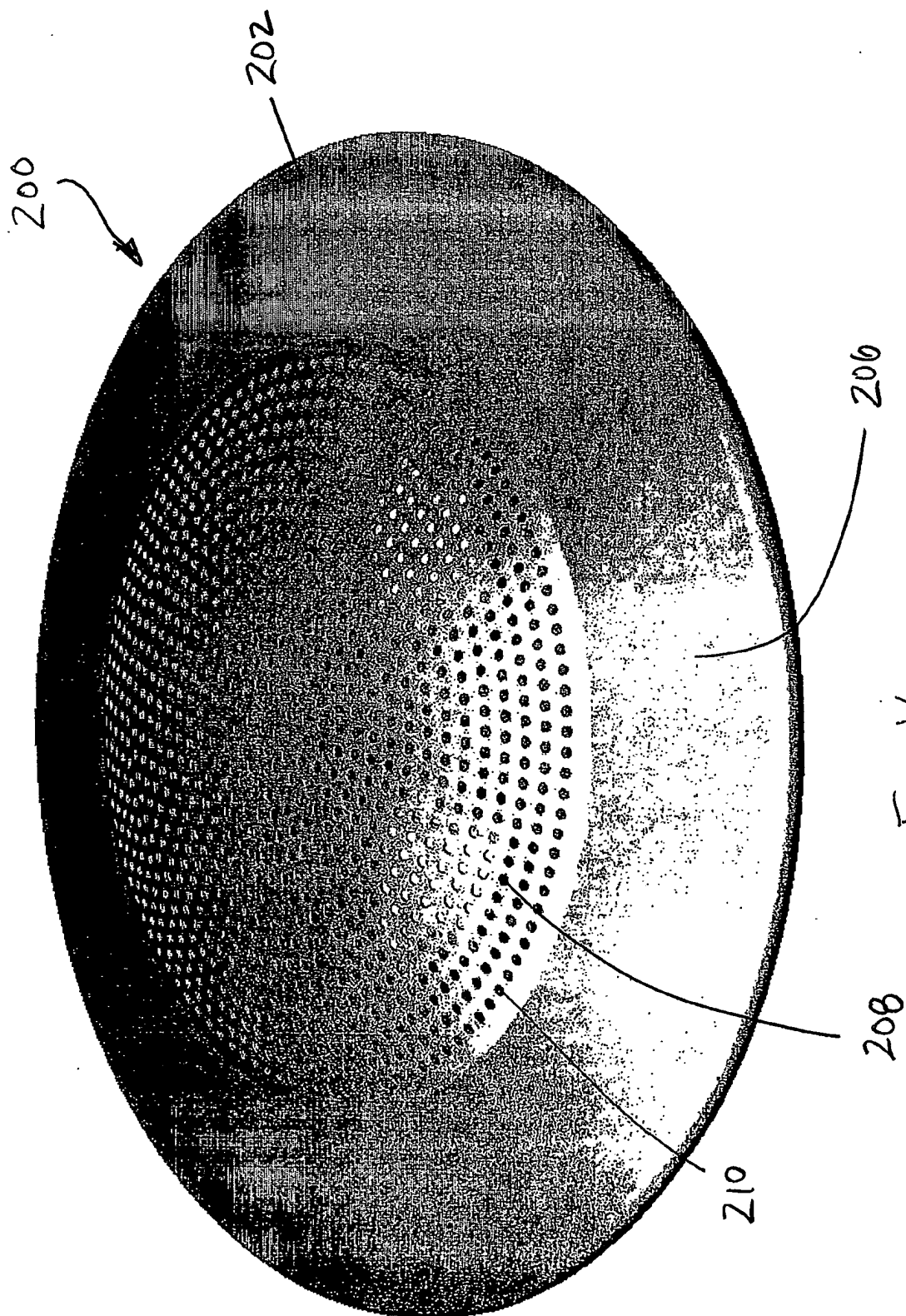


Fig. 16

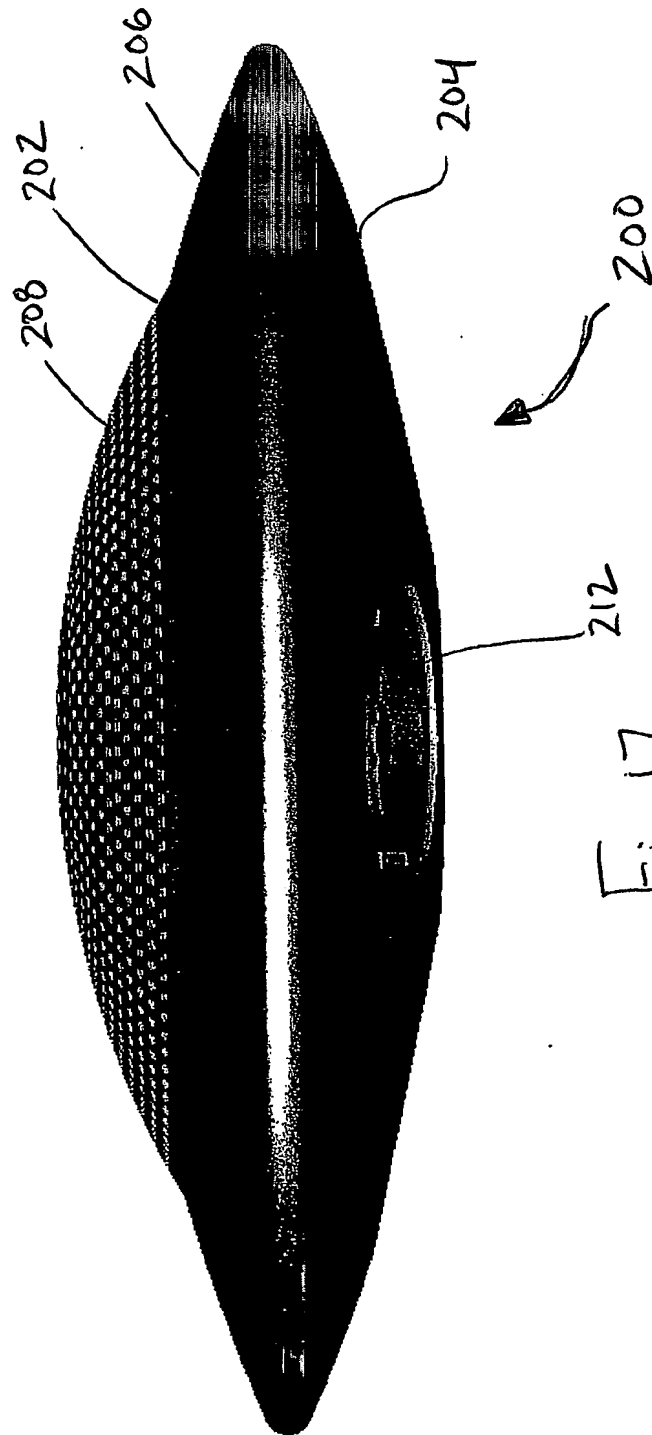
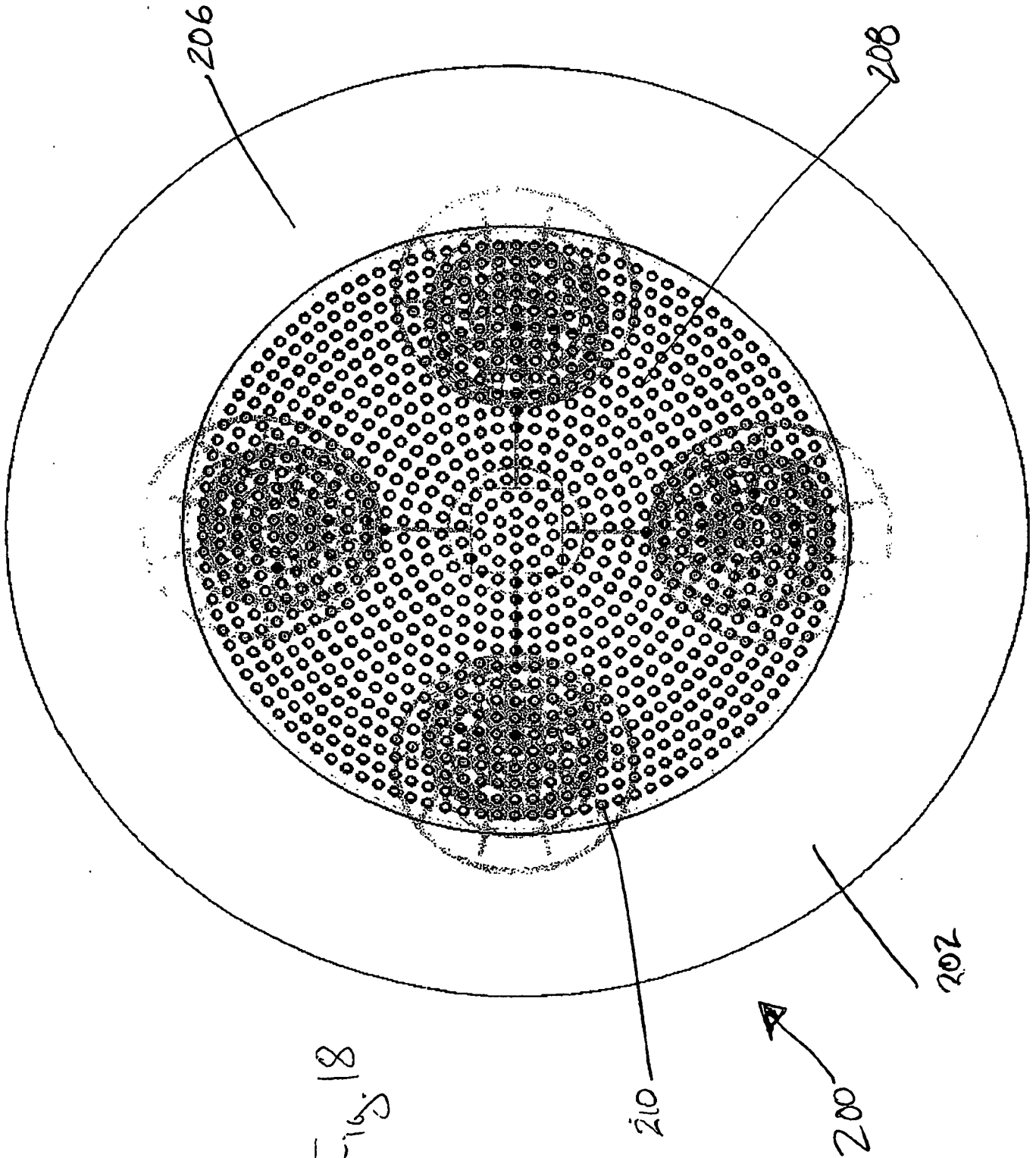


Fig. 17



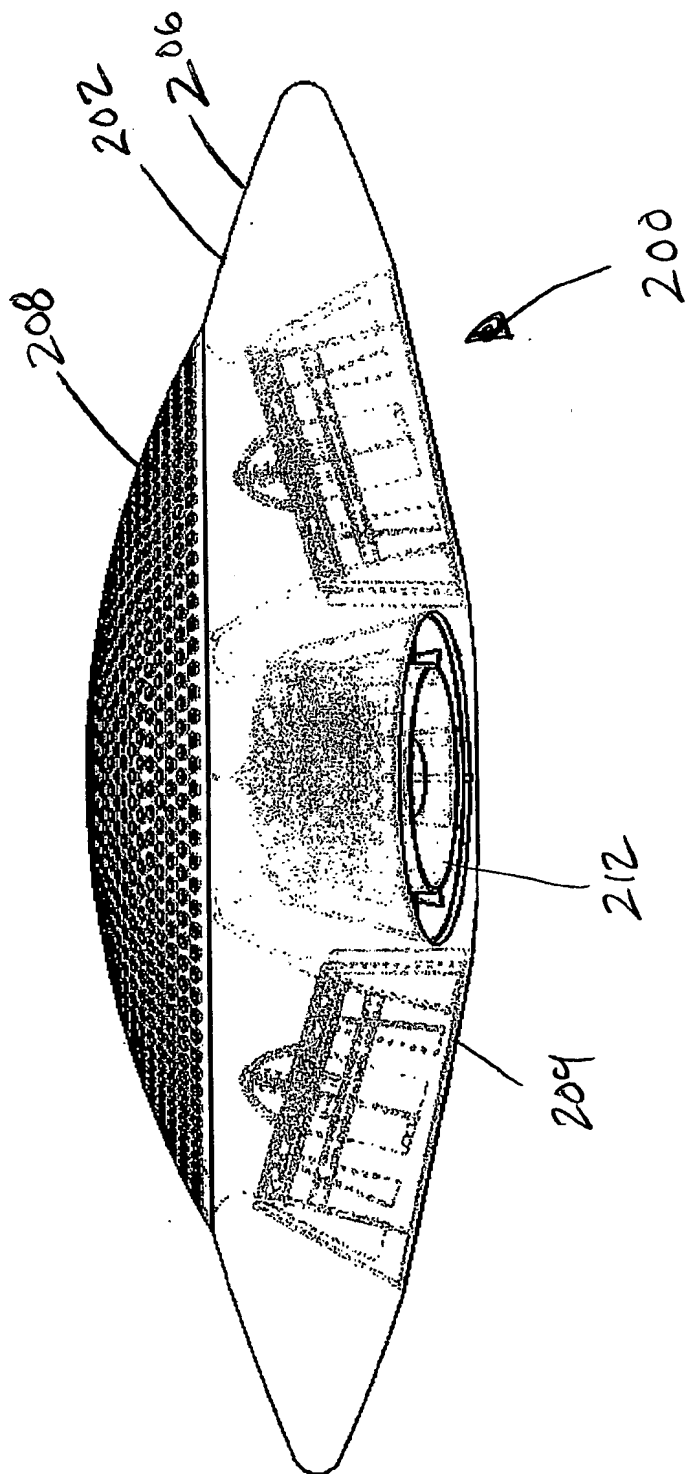


Fig. 19

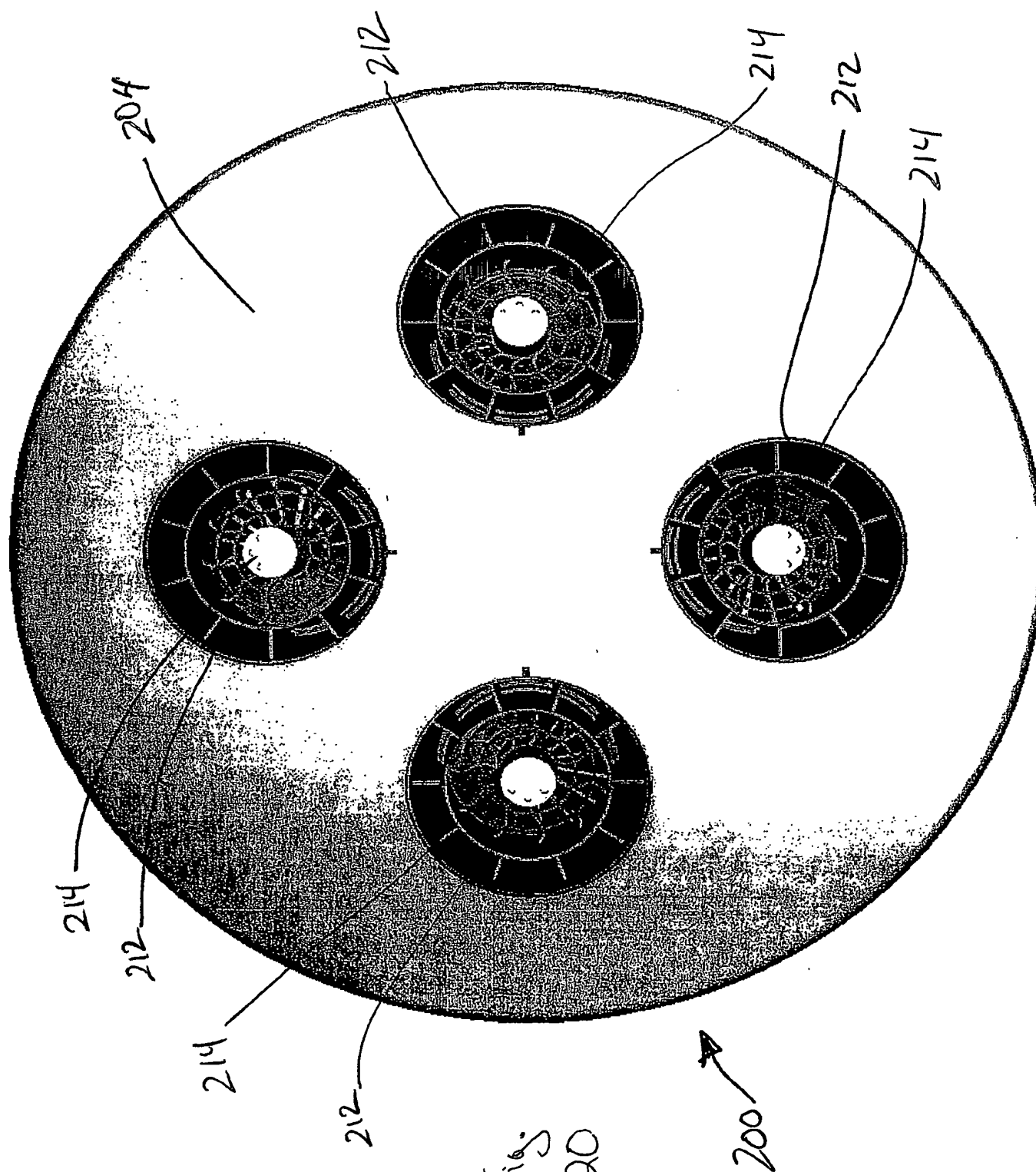


Fig. 20

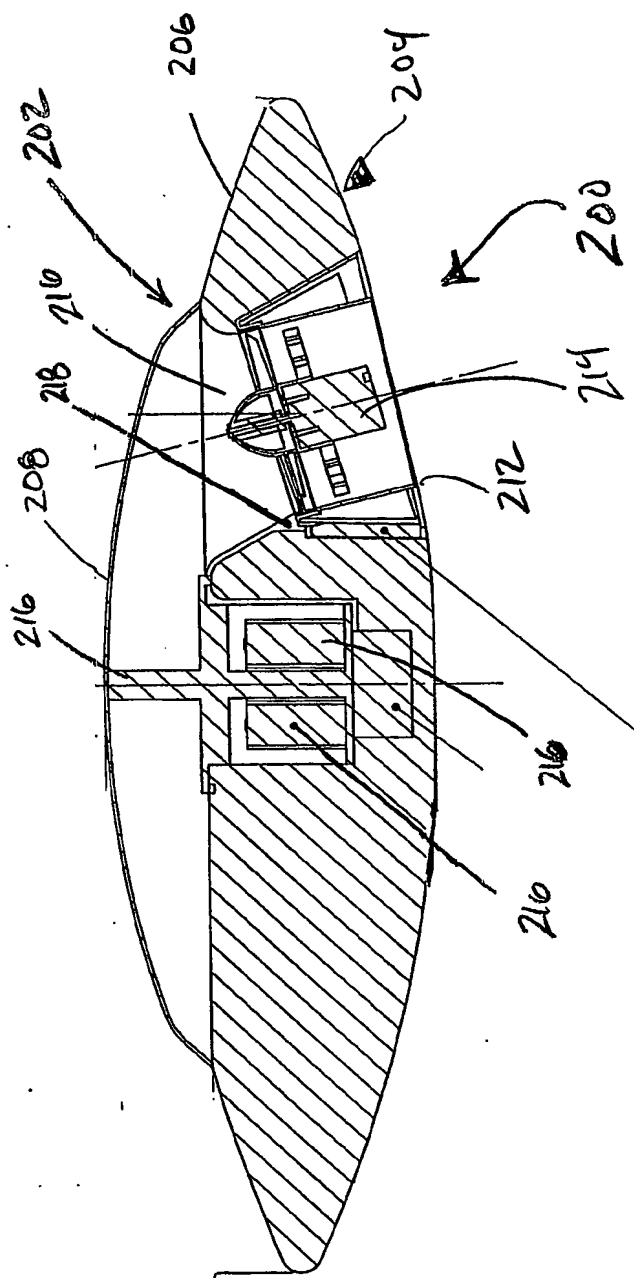
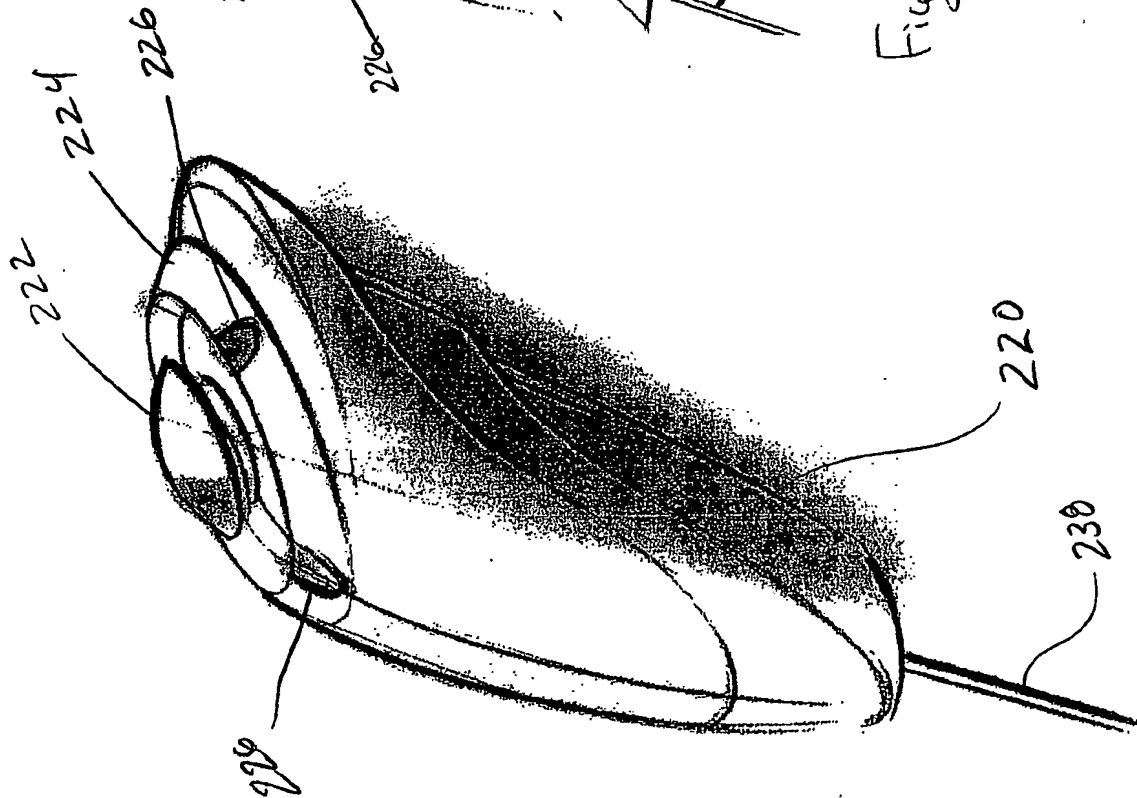
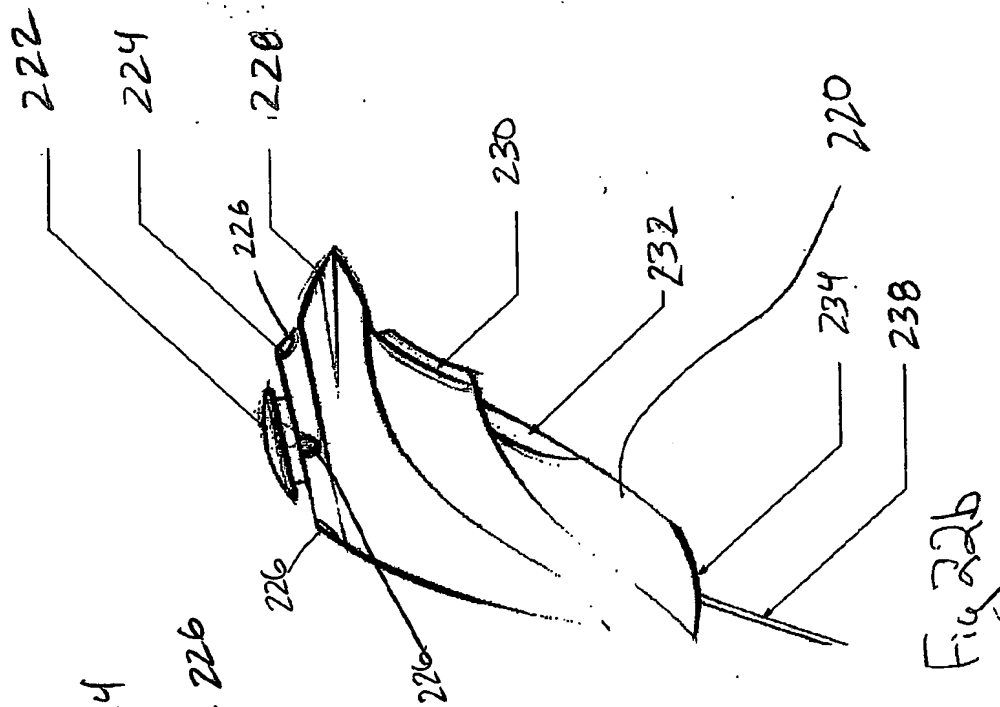
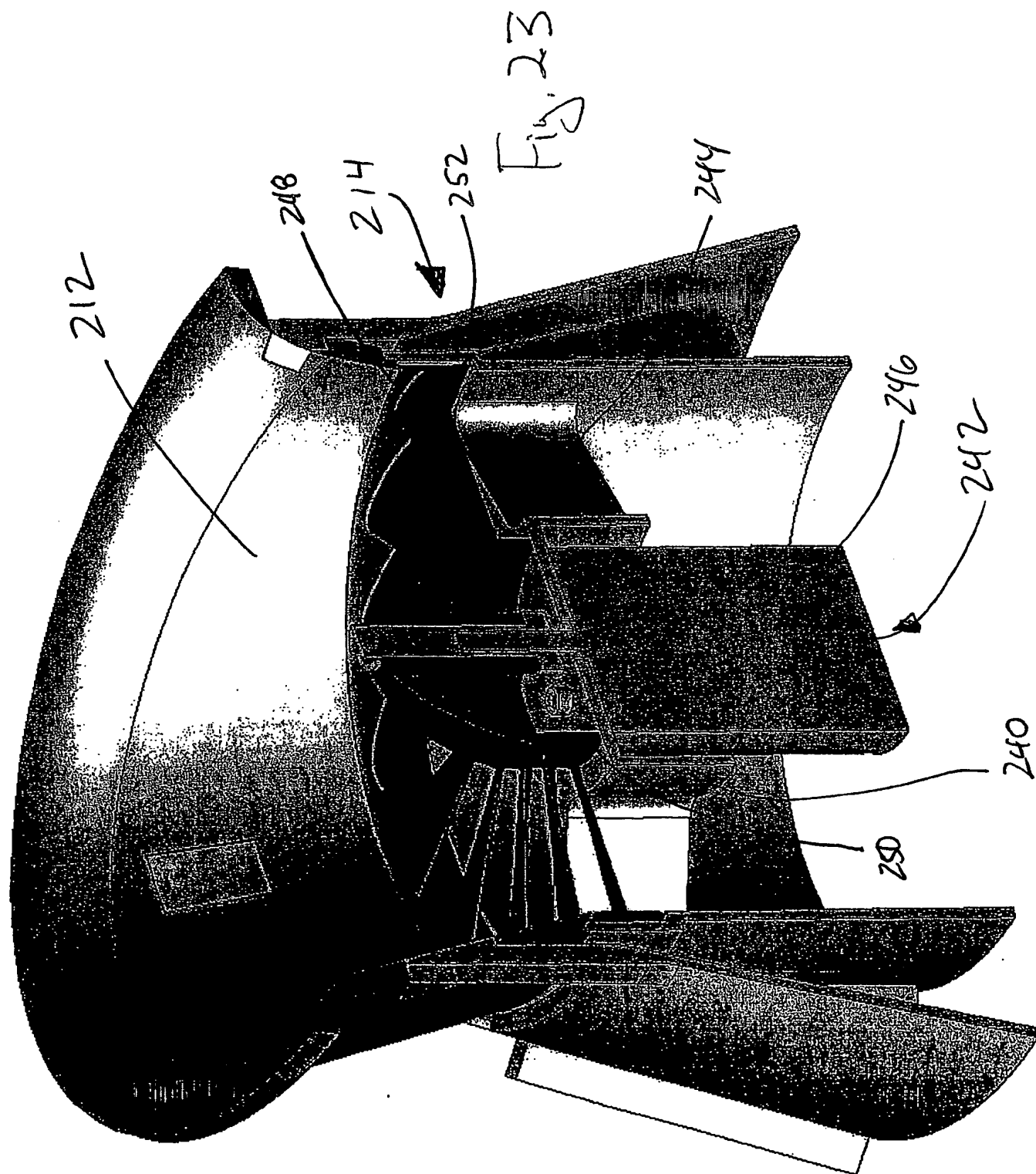


Fig. 21





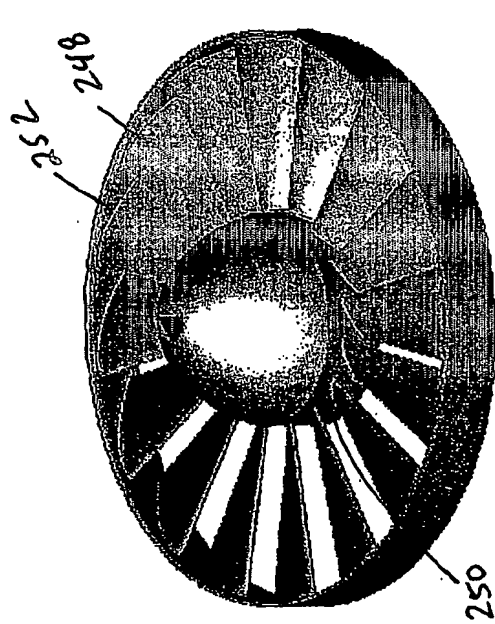
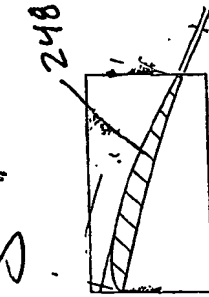
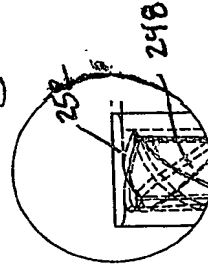


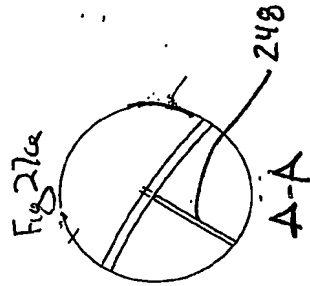
Fig. 24



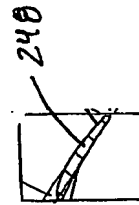
B-B
Fig. 27b



D
Fig. 27d



A-A
248



C-C
Fig. 27c

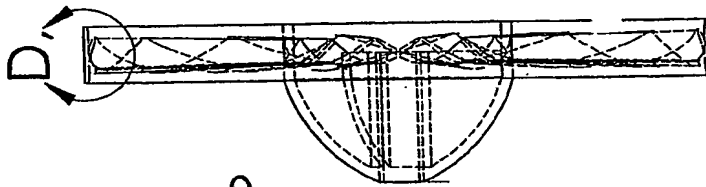


Fig. 26

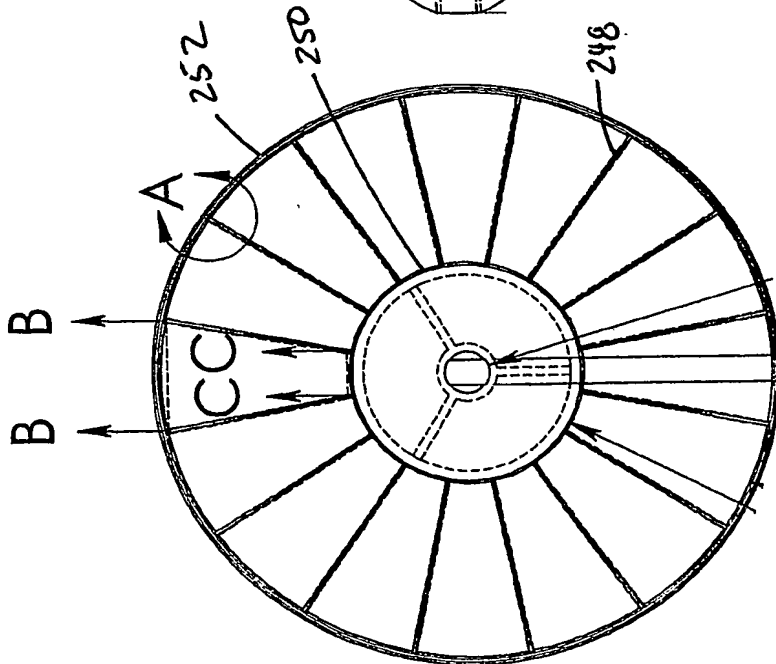


Fig. 25

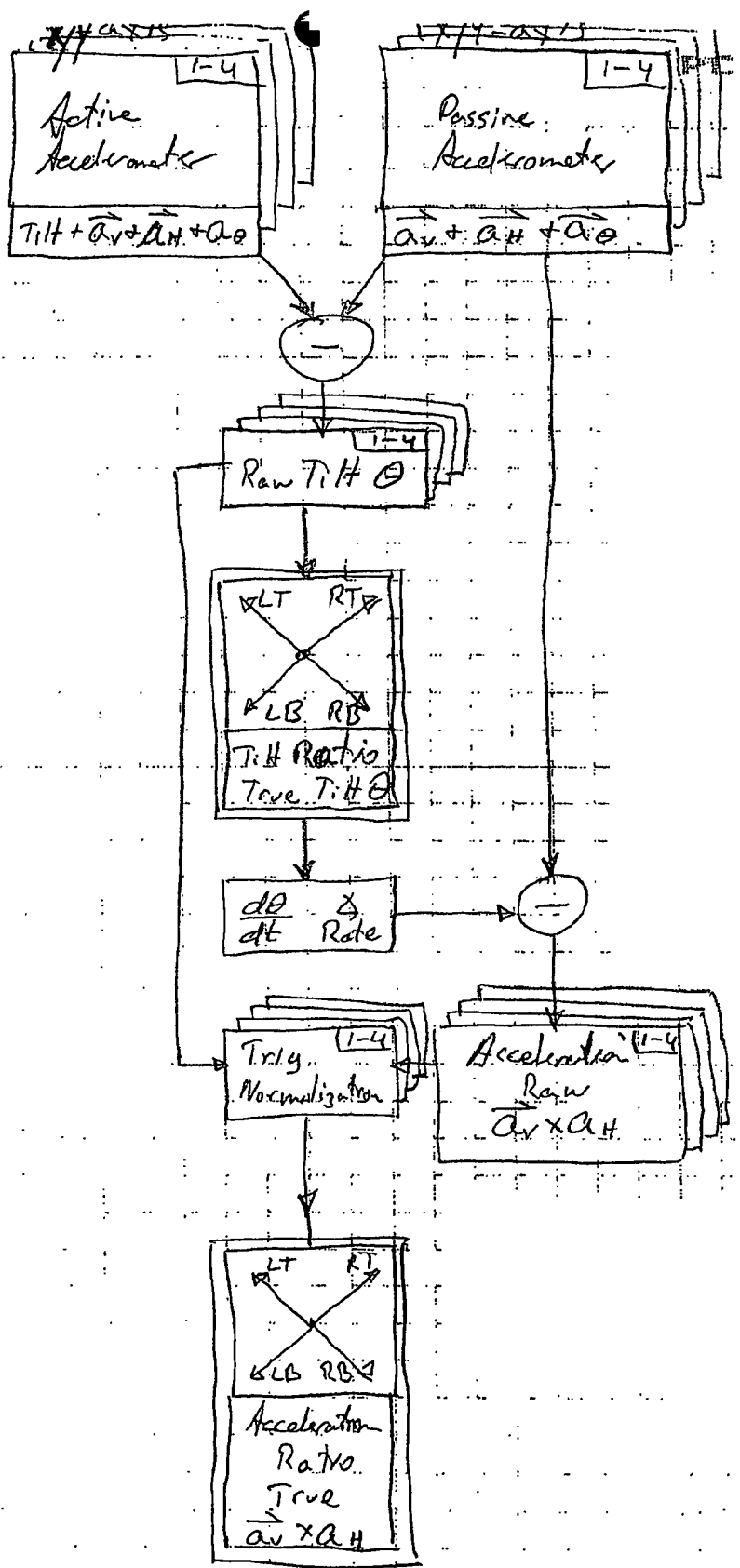


Fig. 28

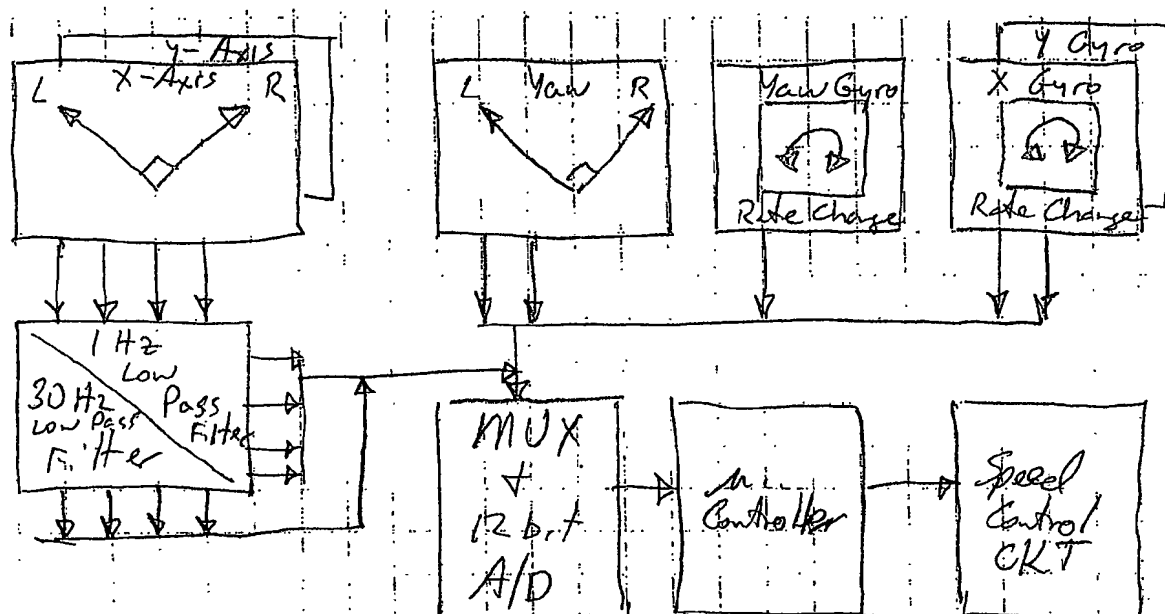
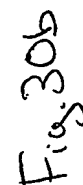
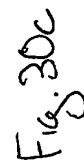



Fig. 29

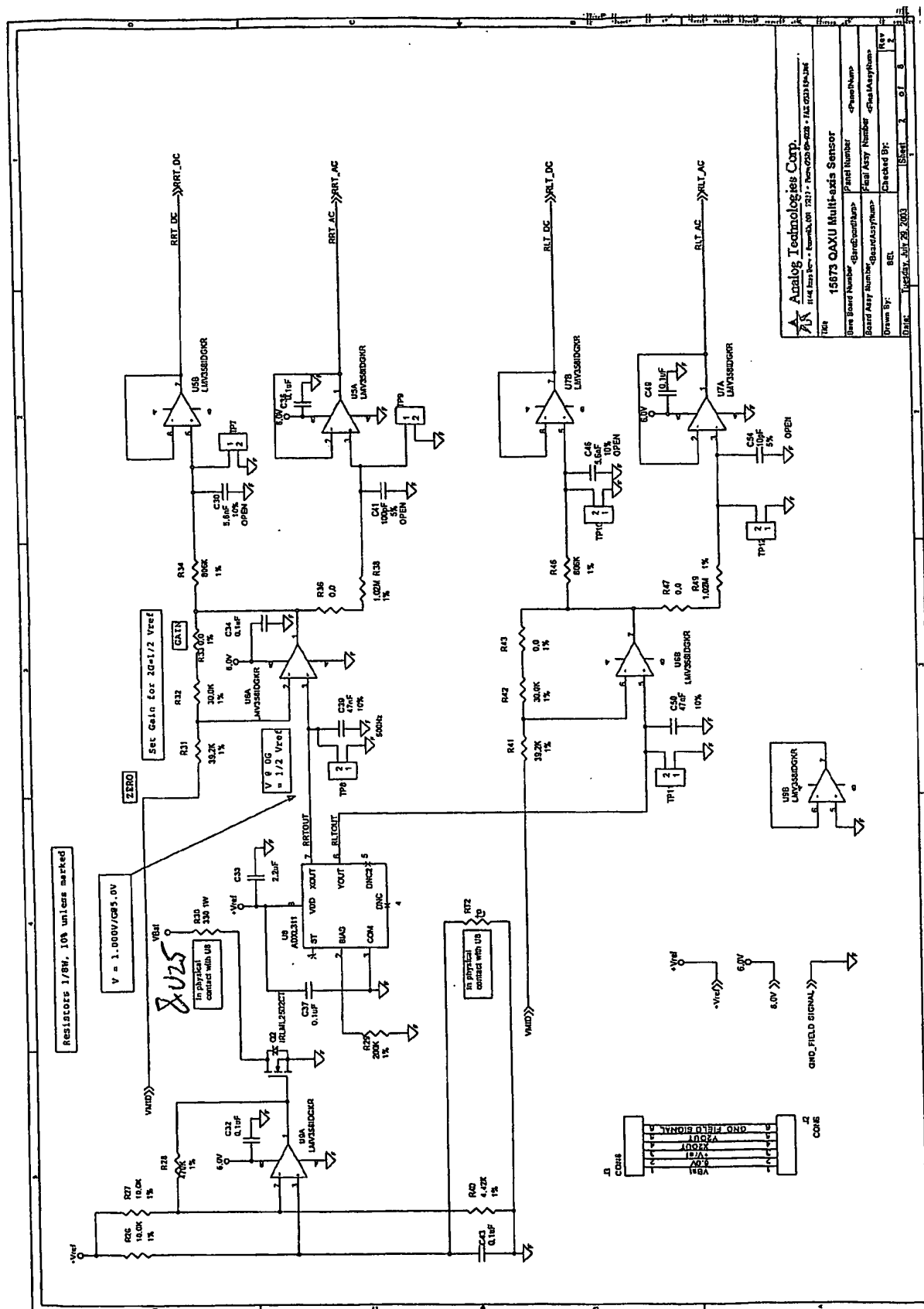






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Board Number: <Board>15673>	Board Assy Number: <Board Assy>15673>	Part Assy Number: <Part Assy>15673>	
Drawn By: REL		Checked By: REL	
Issues: 3 of 3		Revision: 1	

10/526153



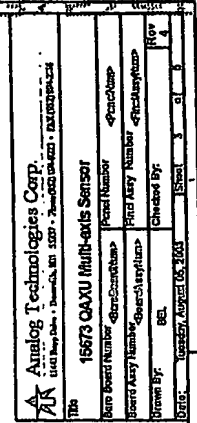


Fig. 30f

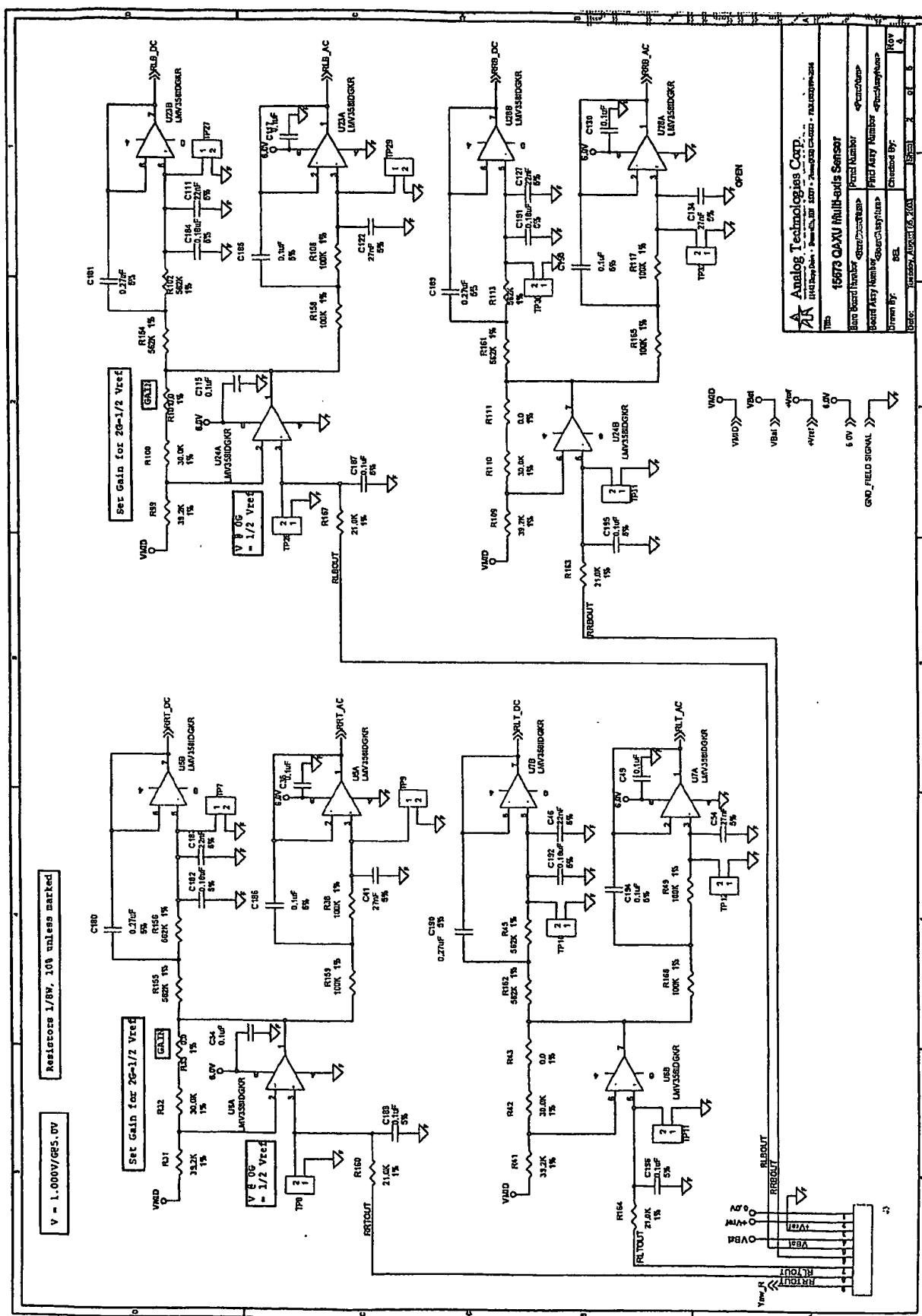


Fig. 30a

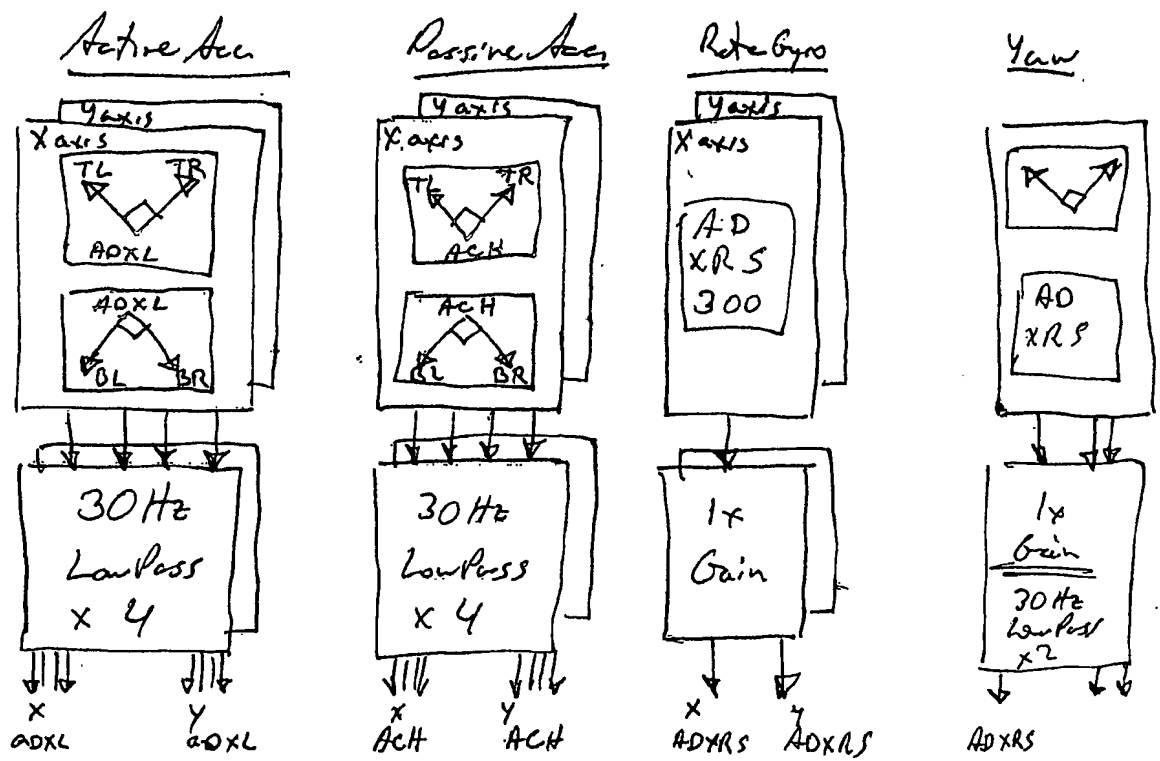


Fig. 31

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